

650V 1.7Ω Super Junction Power MOSFET

Description

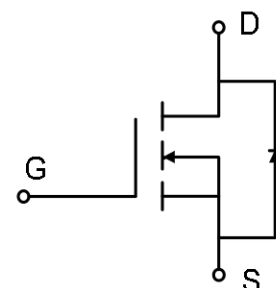
WMOS™ C2 is Wayon's 2nd generation super junction MOSFET family that is utilizing charge balance technology for extremely low on-resistance and low gate charge performance. WMOS™ C2 is suitable for applications which require superior power density and outstanding efficiency.

Features

- $V_{DS} = 700V @ T_{j,max}$
- Typ. $R_{DS(on)} = 1.7\Omega$
- 100% UIS tested
- Pb-free plating, Halogen free

Applications

LED Lighting, Charger, Adapter, PC, LCD TV, Server



Absolute Maximum Ratings

Parameter	Symbol	WMF04N65C2	Unit
Drain-source voltage	V_{DSS}	650	V
Continuous drain current ¹⁾ ($T_C = 25^\circ C$)	I_D	3	A
		1.8	A
Pulsed drain current ²⁾	I_{DM}	5	A
Gate-source voltage	V_{GS}	± 30	V
Avalanche energy, single pulse ³⁾	E_{AS}	11	mJ
Avalanche energy, repetitive ²⁾	E_{AR}	0.05	mJ
Avalanche current, repetitive ²⁾	I_{AR}	0.6	A
Power dissipation ($T_C = 25^\circ C$) - Derate above $25^\circ C$	P_D	4.6	W
		0.04	W/ $^\circ C$
Operating and storage temperature range	T_j, T_{stg}	-55 to +150	$^\circ C$
Continuous diode forward current	I_S	3	A
Diode pulse current	$I_{S,pulse}$	5	A

Thermal Characteristics

Parameter	Symbol	WMF04N65C2	Unit
Thermal resistance, junction-to-case	$R_{\theta JC}$	27	$^\circ C/W$
Thermal resistance, junction-to-ambient	$R_{\theta JA}$	75	$^\circ C/W$

Electrical Characteristics $T_c = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0\text{ V}, I_D=0.25\text{ mA}$	650	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=0.25\text{ mA}$	2.5	3.3	4.5	V
Drain cut-off current	I_{DSS}	$V_{DS}=650\text{ V}, V_{GS}=0\text{ V},$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	-	-	1	μA
Gate leakage current, forward	I_{GSSF}	$V_{GS}=30\text{ V}, V_{DS}=0\text{ V}$	-	-	100	nA
Gate leakage current, reverse	I_{GSSR}	$V_{GS}=-30\text{ V}, V_{DS}=0\text{ V}$	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{ V}, I_D=1\text{ A}$ $T_j = 25^\circ\text{C}$	-	1.7	2.0	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$	-	186	-	pF
Output capacitance	C_{oss}		-	165	-	
Reverse transfer capacitance	C_{rss}		-	3	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 300\text{ V}, I_D = 1\text{ A}$ $R_G = 25\Omega, V_{GS}=10\text{ V}$	-	12	-	ns
Rise time	t_r		-	12	-	
Turn-off delay time	$t_{d(off)}$		-	32	-	
Fall time	t_f		-	18	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	$V_{DD}=480\text{ V}, I_D=1\text{ A},$ $V_{GS}=0\text{ to }10\text{ V}$	-	1.4	-	nC
Gate to drain charge	Q_{gd}		-	3.3	-	
Gate charge total	Q_g		-	6.7	-	
Gate plateau voltage	$V_{plateau}$		-	5	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	$V_{GS}=0\text{ V}, I_F=1\text{ A}$	-	-	1.2	V
Reverse recovery time	t_{rr}	$V_R=50\text{ V}, I_F=1\text{ A},$ $di/dt=100\text{ A}/\mu\text{s}$	-	118	-	ns
Reverse recovery charge	Q_{rr}		-	0.42	-	μC
Peak reverse recovery current	I_{rrm}		-	7	-	A

Notes:

- Limited by $T_{j\text{max}}$. Maximum duty cycle $D=0.5$.
- Repetitive rating: pulse width limited by maximum junction temperature.
- $I_{AS} = 0.6\text{ A}, V_{DD} = 50\text{ V}, R_G = 25\Omega$, starting $T_j = 25^\circ\text{C}$.

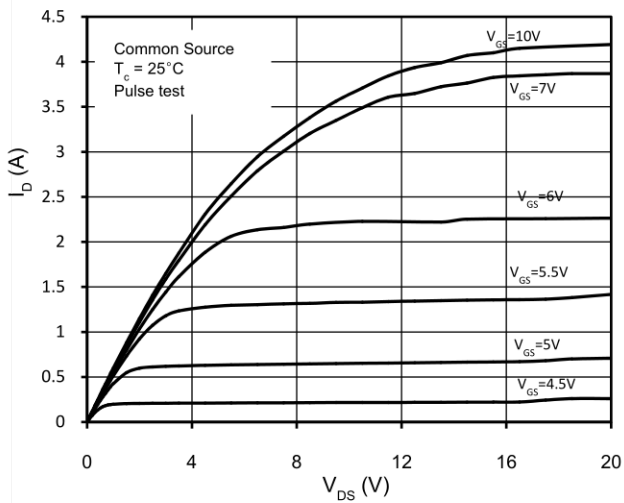


Figure 1. On-Region Characteristics

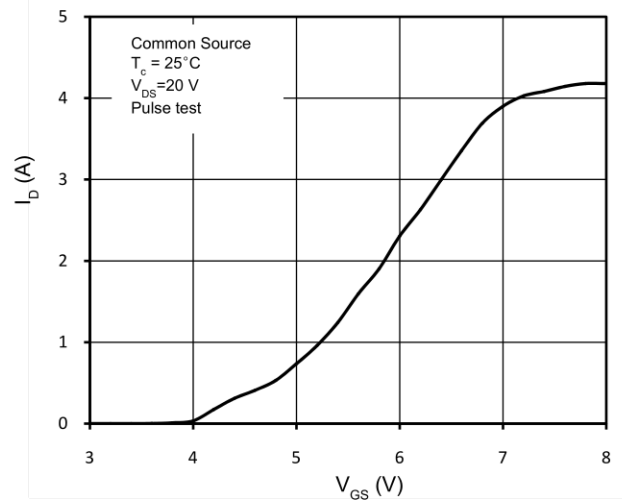


Figure 2. Transfer Characteristics

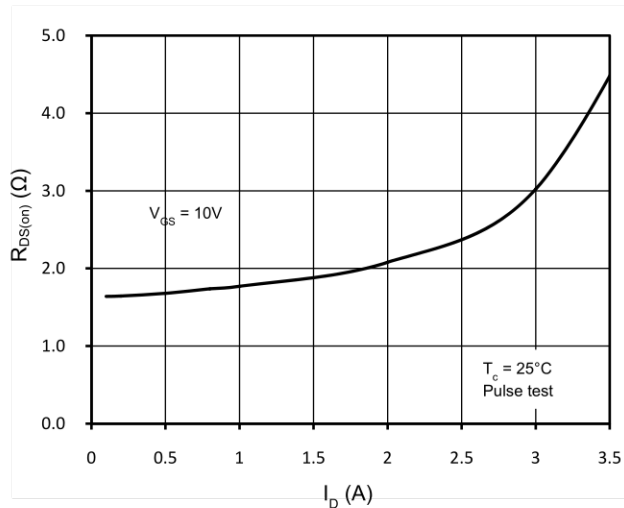


Figure 3. Static Drain-Source On Resistance

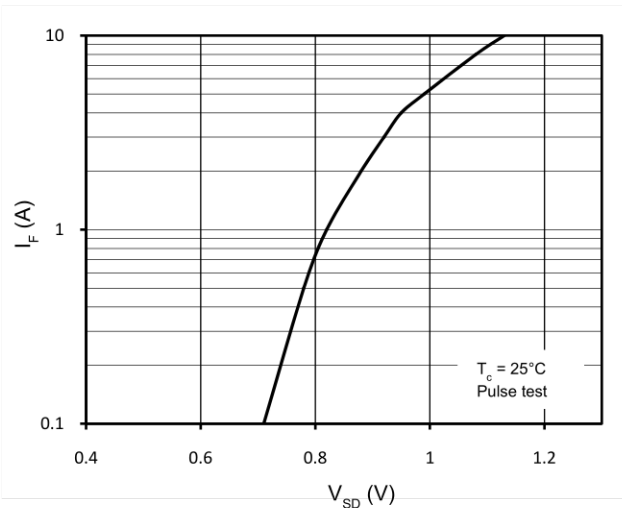


Figure 4. Body-Diode Forward Characteristics

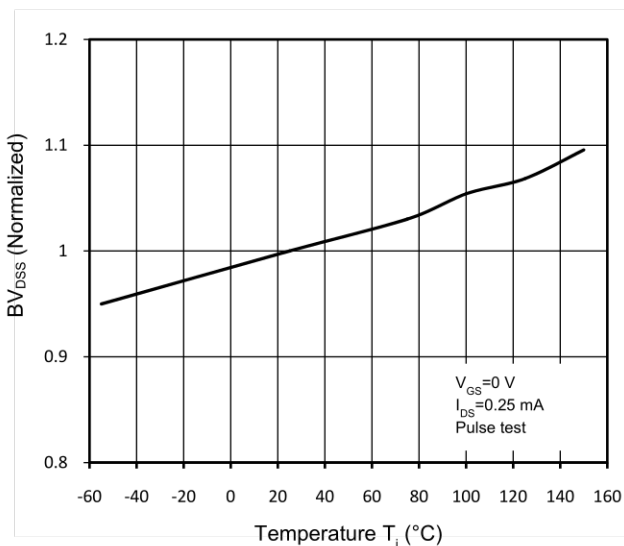


Figure 5. Normalized $BV_{DS(sat)}$ vs. Temperature

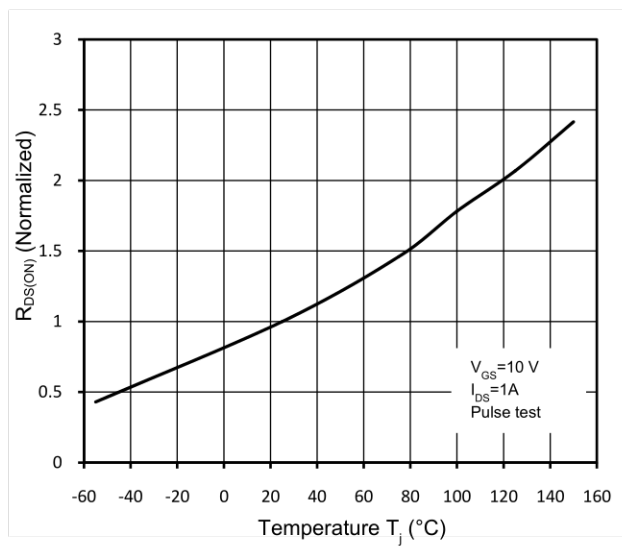


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

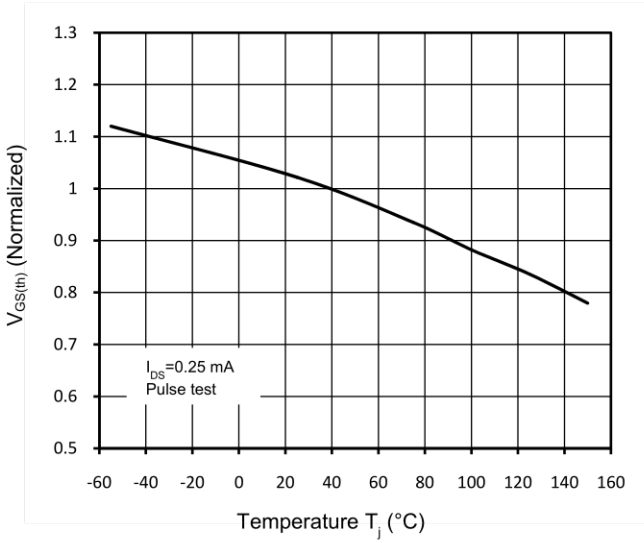


Figure 7. Threshold Voltage vs. Temperature

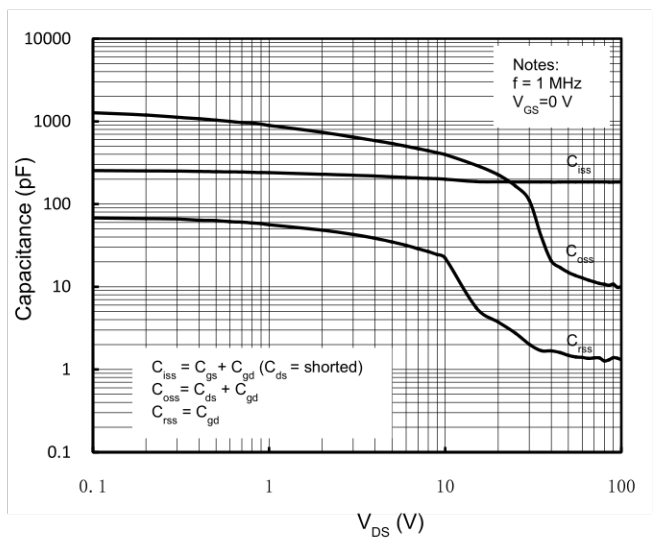


Figure 8. Capacitance Characteristics

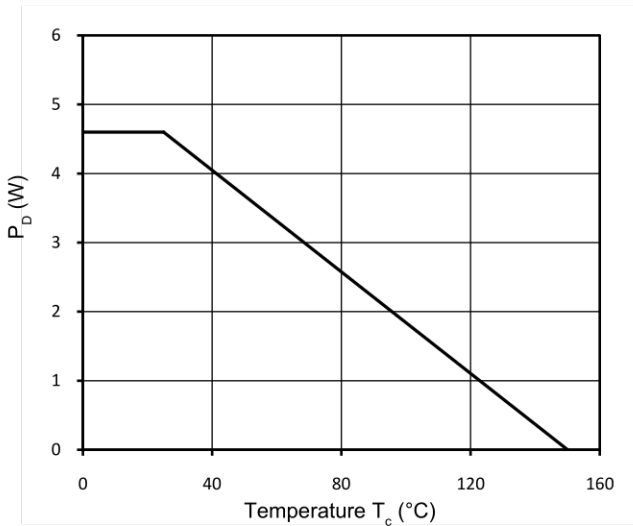


Figure 9. Power Dissipation

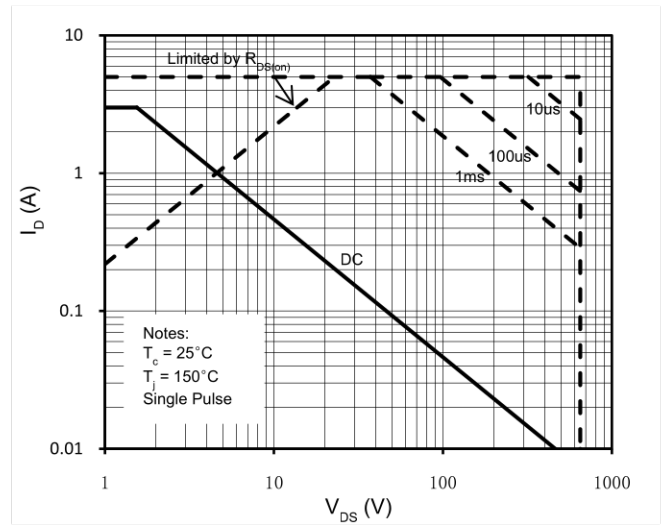


Figure 10. Maximum Safe Operating Area

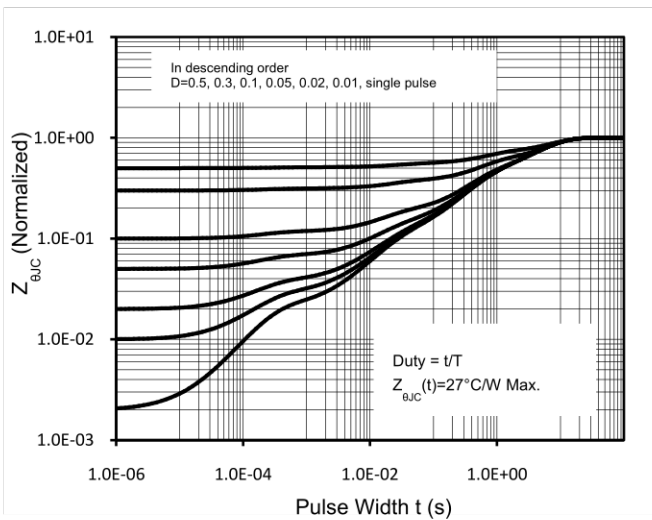


Figure 11. Transient Thermal Response Curve

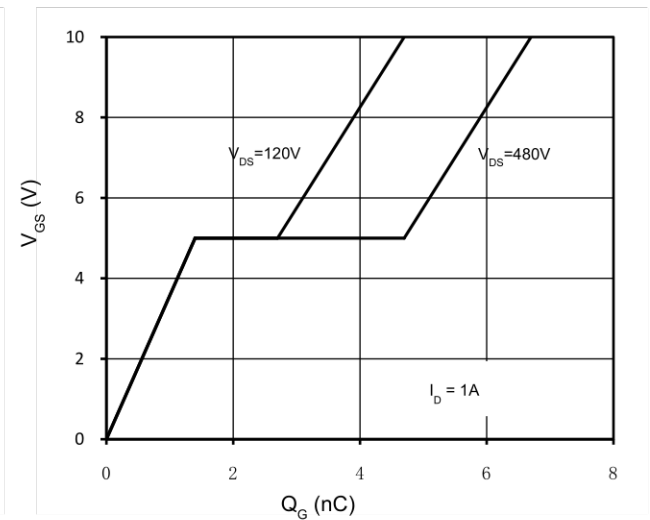
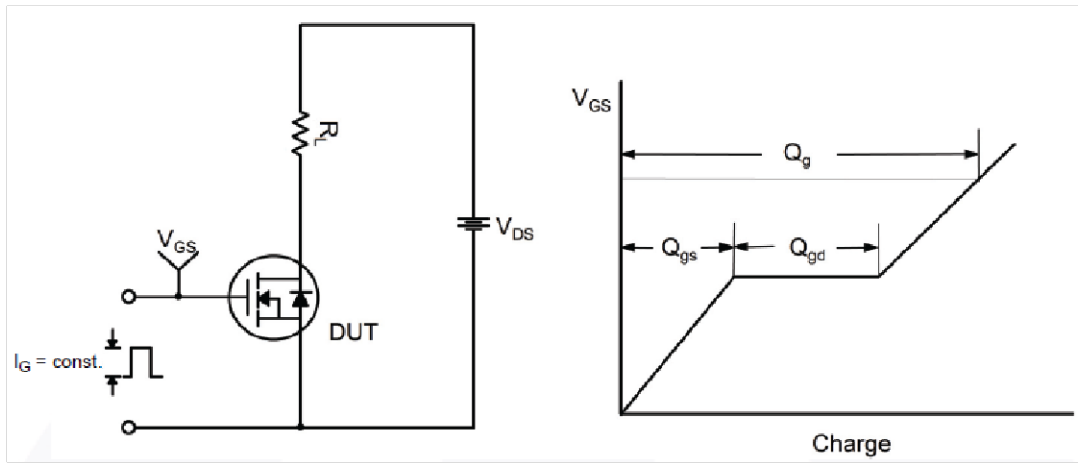
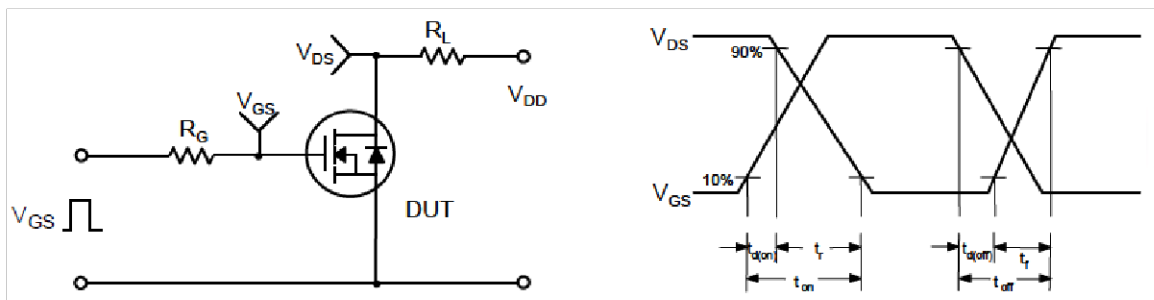


Figure 12. Gate Charge Characteristics

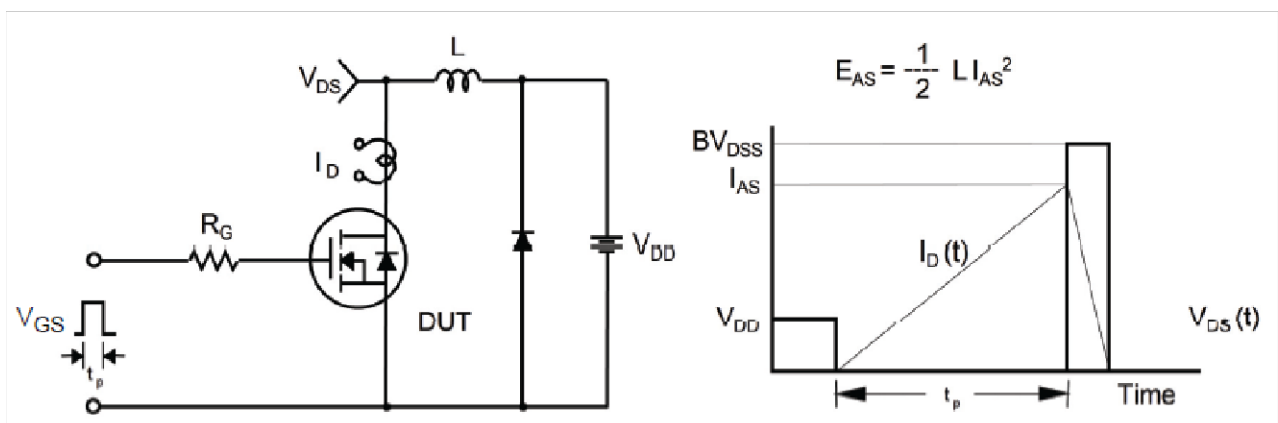
Gate Charge Test Circuit & Waveform

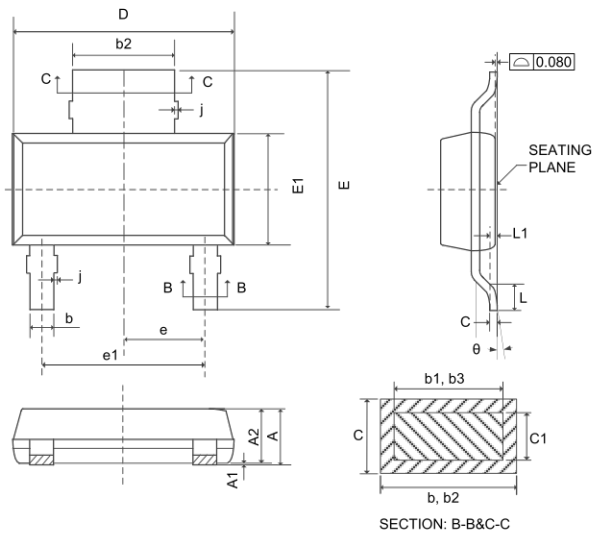


Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



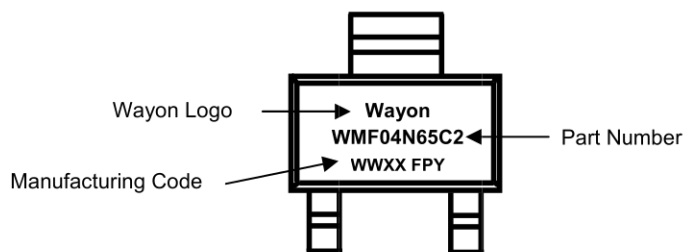
Mechanical Dimensions for SOT-223-2L

COMMON DIMENSIONS

SYMBOL	mm	
	MIN	MAX
A	-	1.80
A1	0.02	0.10
A2	1.50	1.70
b	0.66	0.84
b1	0.6	0.79
b2	2.90	3.10
b3	2.84	3.05
C	0.23	0.35
C1	0.23	0.33
D	6.30	6.95
E	6.70	7.30
E1	3.30	3.70
e	2.30 BSC	
e1	4.60 BSC	
L	0.81	-
L1	0.25 BSC	
theta	0°	10°
j	-	0.13

Ordering Information

Part	Package	Marking	Packing method
WMF04N65C2	SOT-223-2L	WMF04N65C2	Tape and Reel

Marking Information



Contact Information


CYG WAYON CIRCUIT PROTECTION CO., LTD.

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201202

Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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